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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/893,965	06/29/2001	Jeong Hyun Kim	8733.472.00	6105
30827	7590 02/02/2004		EXAMINER	
MCKENNA LONG & ALDRIDGE LLP 1900 K STREET, NW			ORTIZ, EDGARDO	
WASHINGTON, DC 20006			ART UNIT	PAPER NUMBER
			2815	

DATE MAILED: 02/02/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

		A	<u> </u>
•	Application No.	Applicant(s)	
Advisory Action	09/893,965	KIM ET AL.	
	Examiner	Art Unit	
	Edgardo Ortiz	2815	
The MAILING DATE of this communication appe	ears on the cover sheet with the c	orrespondence address	
THE REPLY FILED FAILS TO PLACE THIS APP Therefore, further action by the applicant is required to average final rejection under 37 CFR 1.113 may only be either: (1 condition for allowance; (2) a timely filed Notice of Appea Examination (RCE) in compliance with 37 CFR 1.114.) a timely filed amendment which	ation. A proper reply to a n places the application in	
PERIOD FOR RE	EPLY [check either a) or b)]		
a) \square The period for reply expires 3 months from the mailing date	e of the final rejection.		
b) The period for reply expires on: (1) the mailing date of this a no event, however, will the statutory period for reply expire to ONLY CHECK THIS BOX WHEN THE FIRST REPLY WAS 706.07(f). Extensions of time may be obtained under 37 CFR 1.136(a). The fee have been filed is the date for purposes of determining the period of fee under 37 CFR 1.17(a) is calculated from: (1) the expiration date of (2) as set forth in (b) above, if checked. Any reply received by the Offitimely filed, may reduce any earned patent term adjustment. See 37 C	later than SIX MONTHS from the mailing S FILED WITHIN TWO MONTHS OF THe date on which the petition under 37 CF of extension and the corresponding amo the shortened statutory period for reply ce later than three months after the mail	g date of the final rejection. HE FINAL REJECTION. See MPEP R 1.136(a) and the appropriate extensio unt of the fee. The appropriate extensic originally set in the final Office action; or	n
1. A Notice of Appeal was filed on Appellant's 37 CFR 1.192(a), or any extension thereof (37 CFI			
2. The proposed amendment(s) will not be entered be	ecause:		
(a) \(\square\) they raise new issues that would require further	er consideration and/or search (s	see NOTE below);	
(b) \square they raise the issue of new matter (see Note b	pelow);		
(c) they are not deemed to place the application is issues for appeal; and/or	n better form for appeal by mate	rially reducing or simplifying the	
(d) they present additional claims without cancel NOTE:	ing a corresponding number of fi	nally rejected claims.	
3. Applicant's reply has overcome the following reject	tion(s):		
4. Newly proposed or amended claim(s) would canceling the non-allowable claim(s).	. ,	eparate, timely filed amendment	
5. ☐ The a) ☐ affidavit, b) ☐ exhibit, or c) ☐ request for application in condition for allowance because: Se		dered but does NOT place the	
6. The affidavit or exhibit will NOT be considered bec raised by the Examiner in the final rejection.		o issues which were newly	
 7. For purposes of Appeal, the proposed amendment explanation of how the new or amended claims we 	· · · · —		
The status of the claim(s) is (or will be) as follows:			
Claim(s) allowed:			
Claim(s) objected to:			
Claim(s) rejected: 1-12 and 14-17.			
Claim(s) withdrawn from consideration:			
8. The drawing correction filed on is a) app	roved or b) disapproved by t	he Examiner.	
9. Note the attached Information Disclosure Statemen	nt(s)(PTO-1449) Paper No(s)	·	
10. Other:			

Applicant's arguments have been fully considered but are not deemed persuasive. Applicant argues that the cited references do not disclose or suggest "a plurality of organic pixel electrodes on the first substrate" and "the organic pixel electrodes connect to the thin film transistor through the contact hole". However, the examiner disagrees and notes that, as stated in the rejection, Applicant's admitted prior art teaches a first substrate (21), a plurality of pixel electrodes (29) on the first substrate, a passivation layer (27) over a thin film transistor, wherein the passivation layer includes a contact hole (Specification, page 4, lines 6-8) and a pixel electrode (29) forned in an active area, wherein the pixel electrode connects to the thin film transistor through the contact hole as shown in prior art figure 1.

Applicant also argues that "AGFA discusses the use of PEDOT in the context of electroluminescent lamps only. AGFA does not suggest at all that PEDOT is suitable for use in LCD displays such as those of the present application." The examiner disagress with Applicant's assertion of the AGFA reference and notes that AGFA clearly discloses that PEDOT is used for transparent electrodes in thin film circuits, such as the ones in Applicant's claimed invention, and therefore AGFA clearly suggests their use in a display device as claimed, see section "PEDOT Screen Printable Pastes" of the reference. Additionally, the reference states that PEDOT can be used in "Electrodes for small flexible flat panel displays" and "High resolution patterned electrodes for plastic electronics (transistors)", thus suggesting the use of PEDOT in an invention as claimed by Applicant.

Tom Thomas Supervisory Patent Examiner Technology Genter 2800